FIG. 1

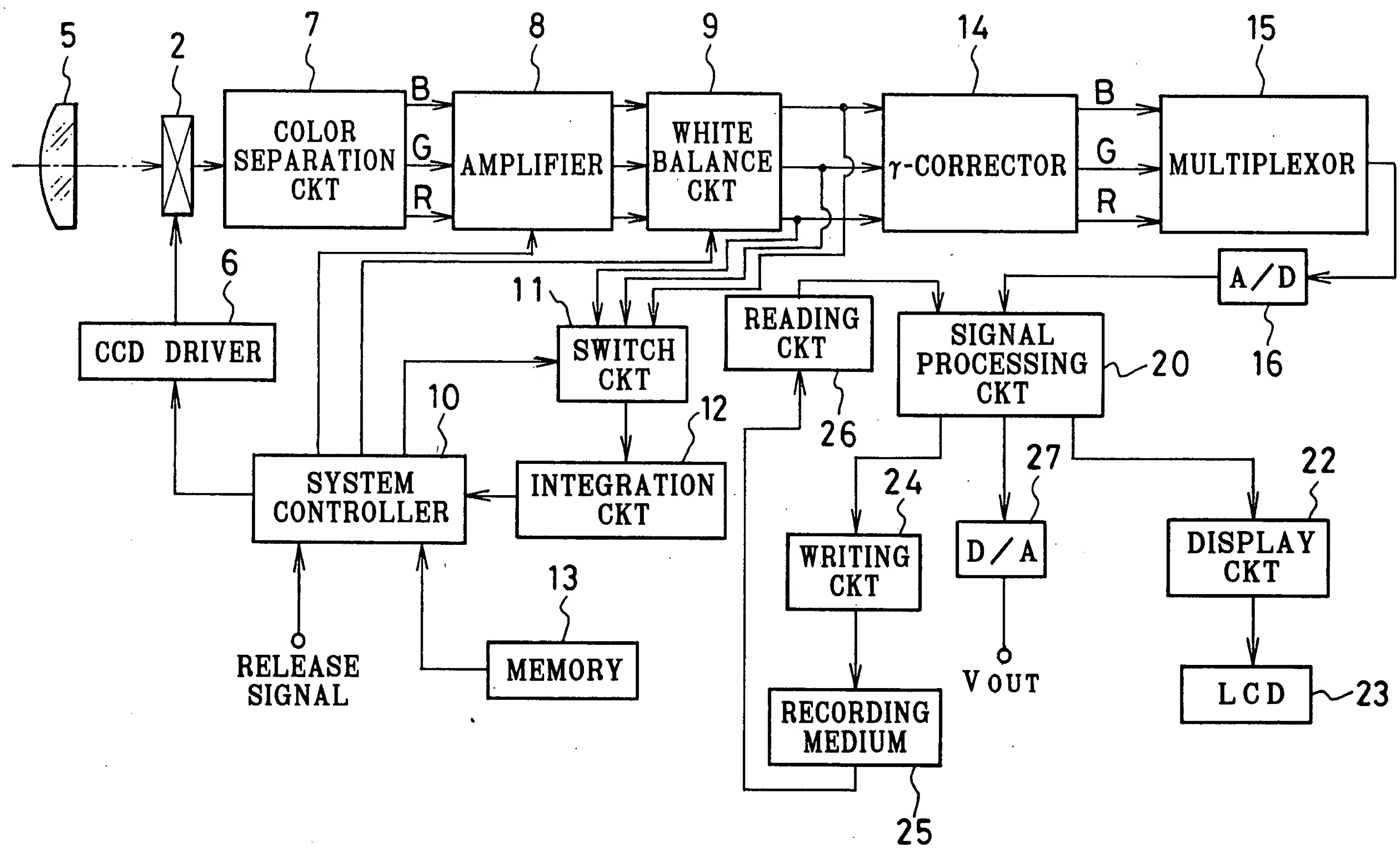

5
2
7
COLOR SEPARATION CKT
B
G
R
8
AMPLIFIER
9
WHITE BALANCE CKT
14
γ-CORRECTOR
B
G
R
15
MULTIPLEXOR
A/D
16
6
CCD DRIVER
11
SWITCH CKT
READING CKT
SIGNAL PROCESSING CKT
20
10
12
26
SYSTEM CONTROLLER
INTEGRATION CKT
27
22
24
WRITING CKT
D/A
DISPLAY CKT
13
RELEASE SIGNAL
MEMORY
V OUT
LCD
23
RECORDING MEDIUM
25

FIG. 2

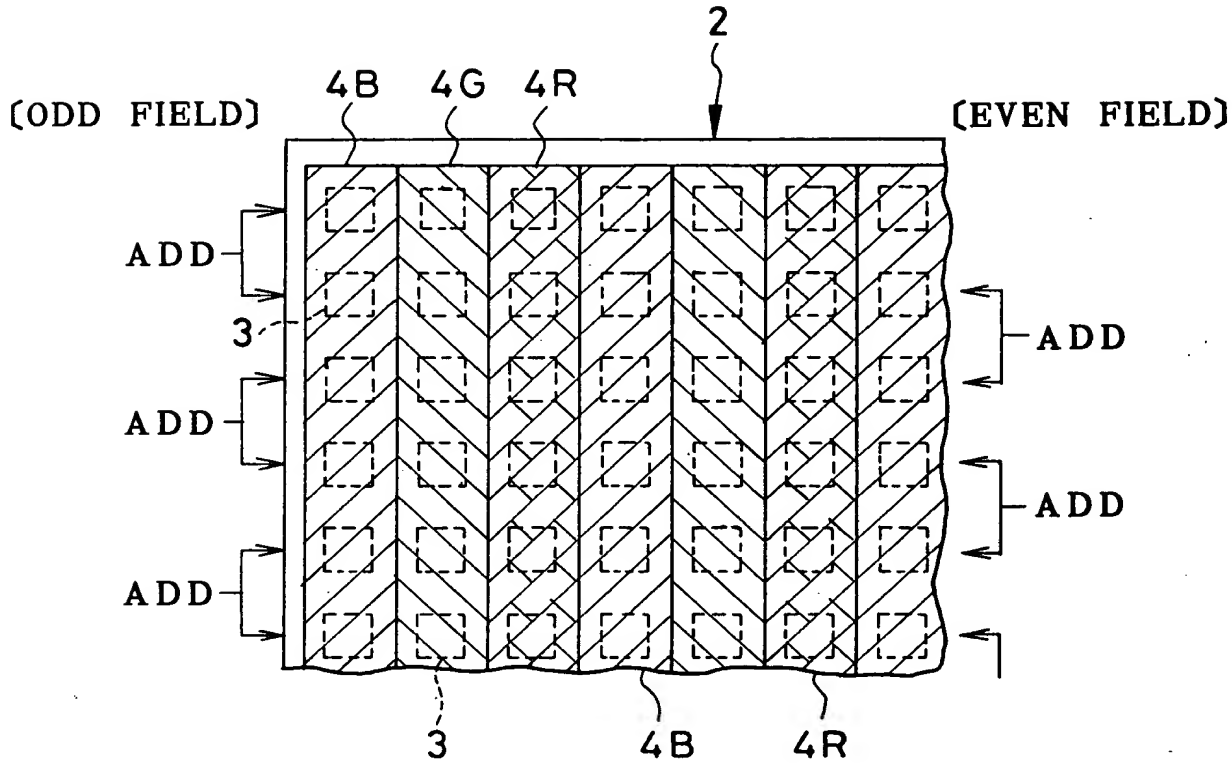

2
4B
4G
4R
(ODD FIELD)
(EVEN FIELD)
ADD
ADD
ADD
ADD
ADD
3
3
4B
4R

FIG. 3

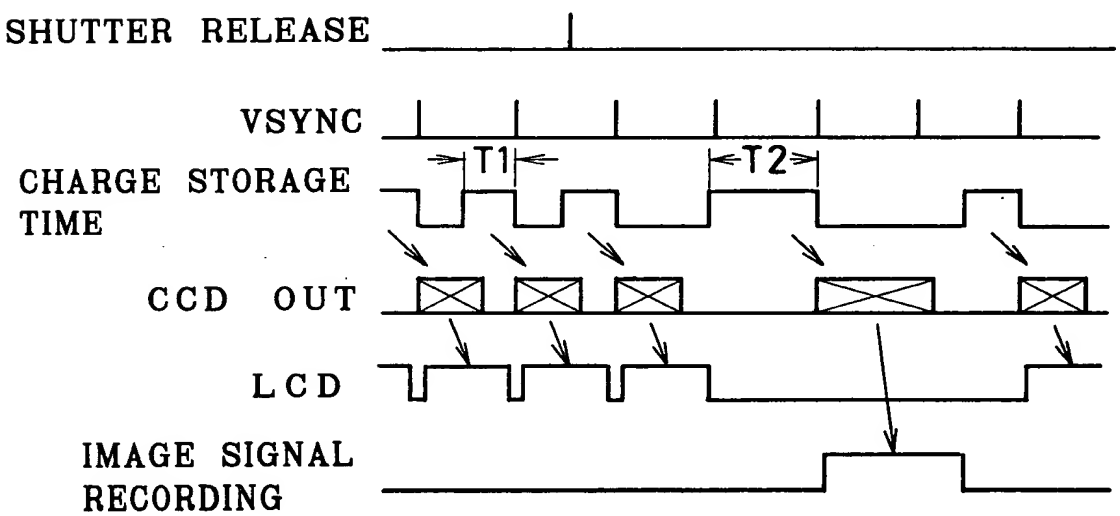
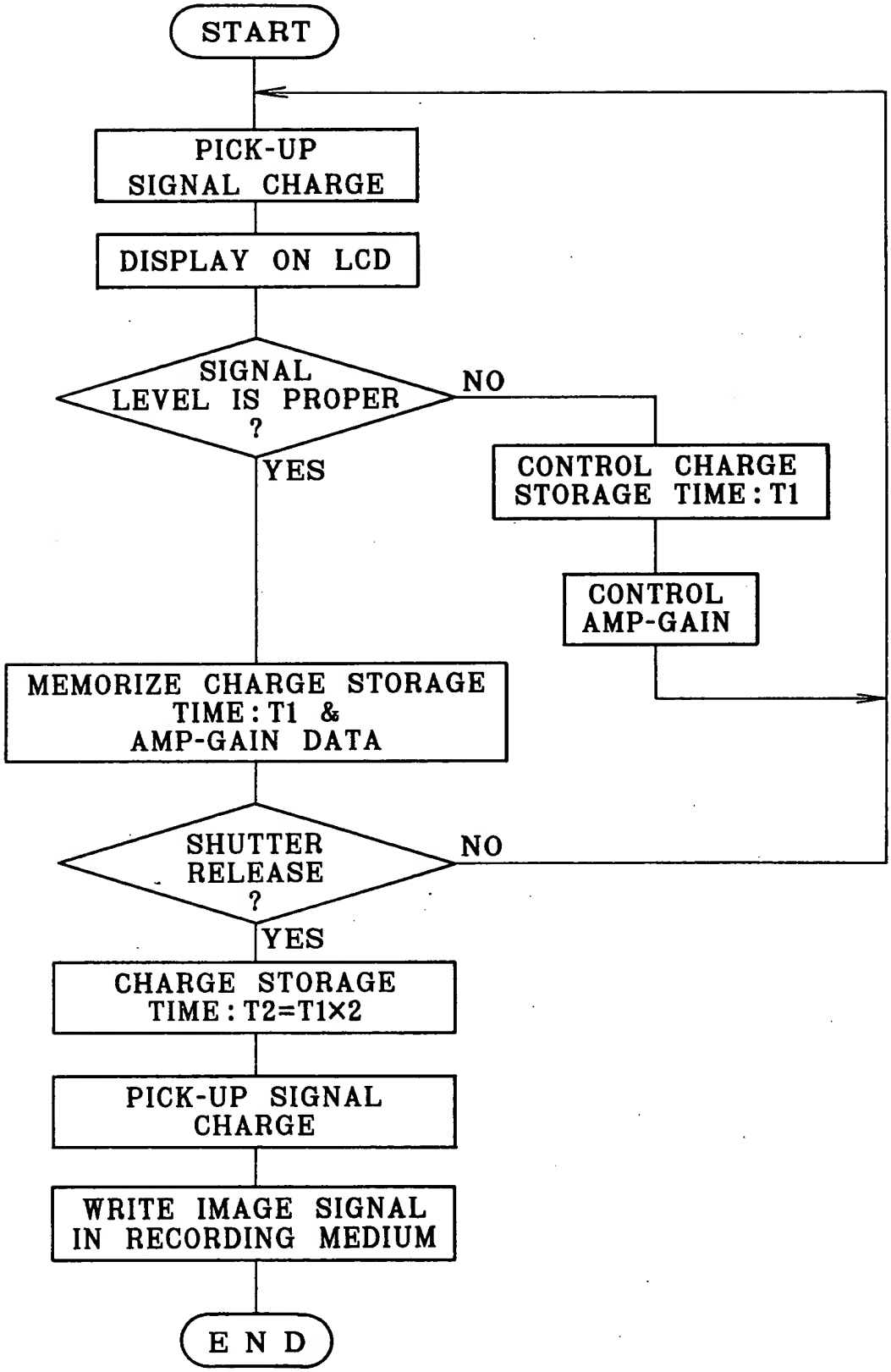

SHUTTER RELEASE
VSYNC
T1
T2
CHARGE STORAGE TIME
CCD OUT
LCD
IMAGE SIGNAL RECORDING

FIG. 4


START
PICK-UP SIGNAL CHARGE
DISPLAY ON LCD
SIGNAL LEVEL IS PROPER ?
NO
YES
CONTROL CHARGE STORAGE TIME : T1
CONTROL AMP-GAIN
MEMORIZE CHARGE STORAGE TIME : T1 & AMP-GAIN DATA
SHUTTER RELEASE ?
NO
YES
CHARGE STORAGE TIME : T2=T1×2
PICK-UP SIGNAL CHARGE
WRITE IMAGE SIGNAL IN RECORDING MEDIUM
E N D